

SEARCHED

Class	Sub.	Date	Exmr.
703	15	12-19	Dmc
updated		5-27-03	Dmc
716	18	12-8-03	Dmc
1	4	1	1
703	15	12-8-03	
703	15	5-16-04	Dmc
	UPDATE		

INTERFERENCE SEARCHED

Class	Sub.	Date	Exmr.

SEARCH NOTES (INCLUDING SEARCH STRATEGY)

	Date	Exmr.
behavioral model		
process block	12/19	Dmc
electronic design		
assignment decision		
diagram convert		
simulate		
MDL Stimulus		
gatech parsetree		
control node		
IEEE		
Assignment		
decision		
diagram		
simulate		
simulator		
Block design	5-27-03	Dmc
edit internal		
Circuit		
Ted Ingberg	12-8-03	Dmc
ACM PORTAL		
Simulation	12-8-03	Dmc
Ternary		
Assignment	12-8-03	Dmc
Decision		
Diagram		
IEEE		
IEEE	12-8-03	Dmc
Chaiyakul		
IEEE	12-8-03	Dmc
Control flow		
Simulation		
graph and state		
EAST setting state	12-8-03	Dmc
annotating control		
node condition		
State Synthesis		
IEEE	12-8-03	Dmc
Condition node		
STATE behavioral		